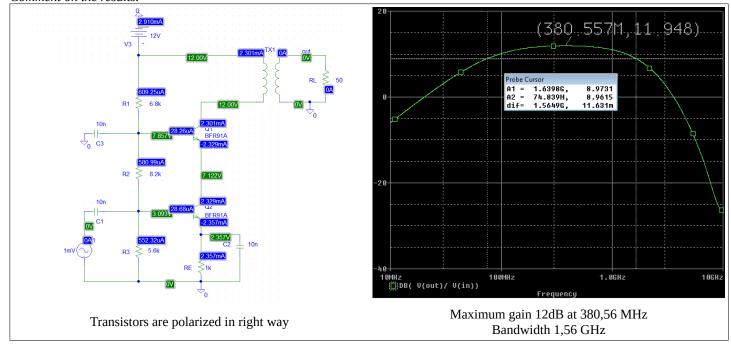
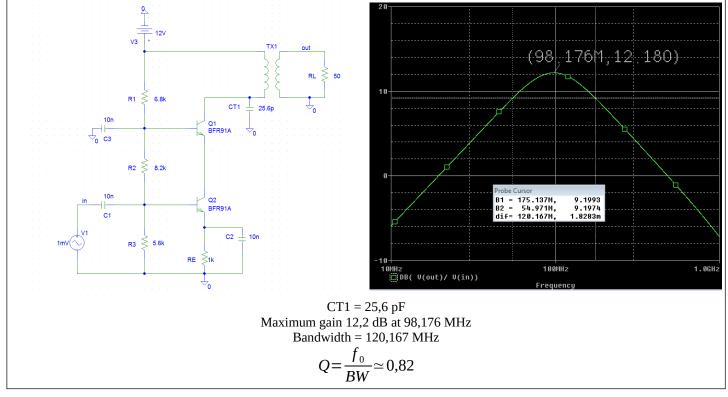
POINT 1

Draw the cascode circuit and insert the output transformer placing an "XFRM_LINEAR" component. Set the value of inductances L1 and L2 in order to have an unitary transformation ratio with an inductance of 100nH. Check the bias operation point and, by means of an AC analysis, determine the midband voltage gain of the amplifier. Comment on the results.



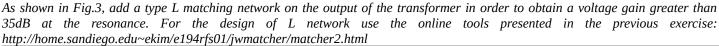


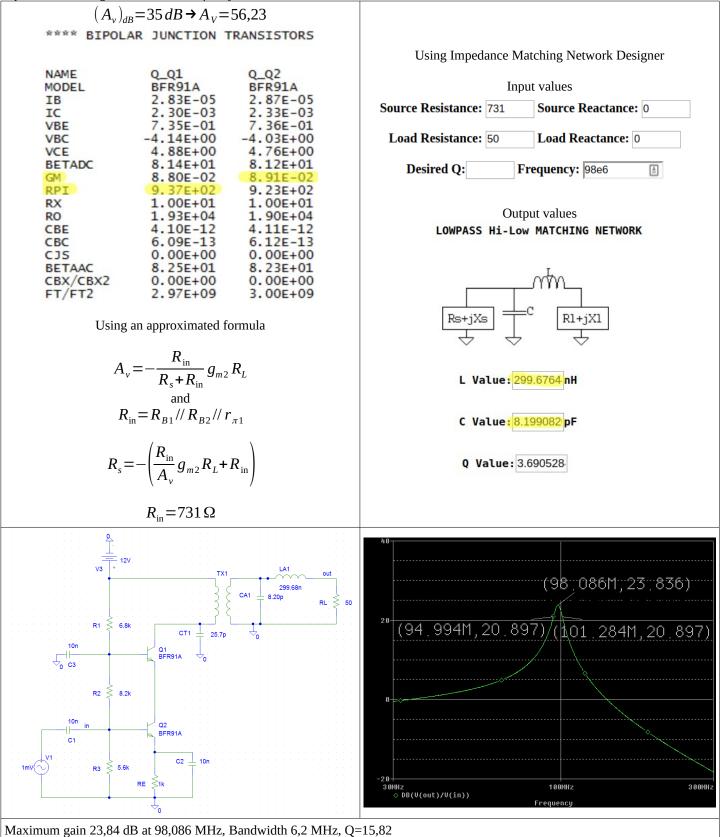
Insert the tuning capacitor CT1 imposing a center frequency of 98MHz. Plot the frequency response of the amplifier and measure the resonance frequency, the maximum gain and the quality factor Q of the circuit.



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POINT 3

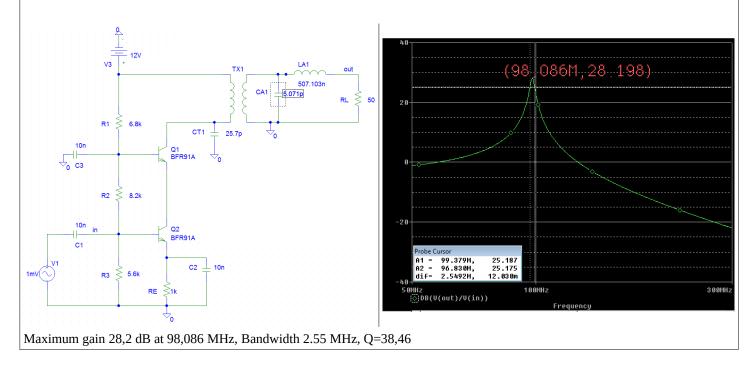




You can note that the maximum gain is 24 dB. This behavior arise from approximations in expression of gain.

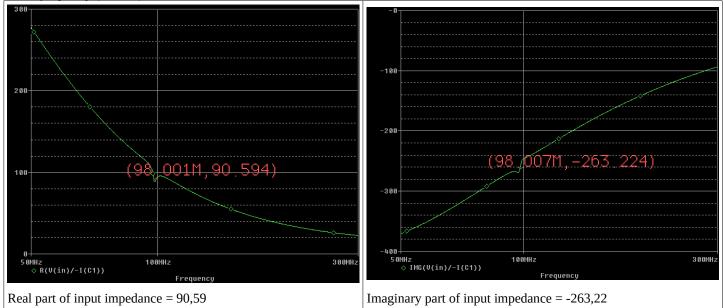
The influence of the output resistance should be inserted increasing the value of Rin (called Rs in the online tool).		
Executing several simulation you can obtain the following results		
Rs (Ohm)	Gain (dB)	BW (MHz)
731	23.80	6.20
1000	25.20	4.60
2000	28.10	2.55
5000	32.00	
10000	35.10	0.56

Using configuration with 35dB of gain the bandwidth is too selective, for this reason the configuration with 28 dB is used in the following development.



POINT 4

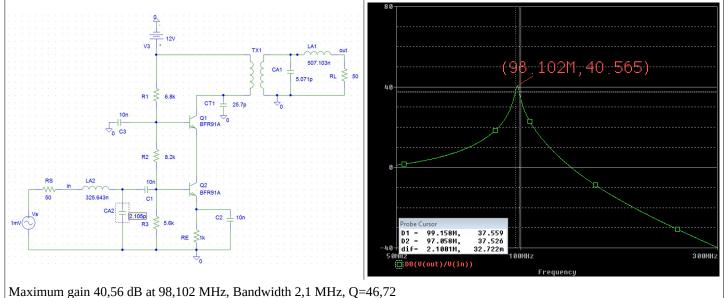
After the design of the output matching network, determine the real and imaginary part of the input impedance of the circuit at the center frequency (98MHz).



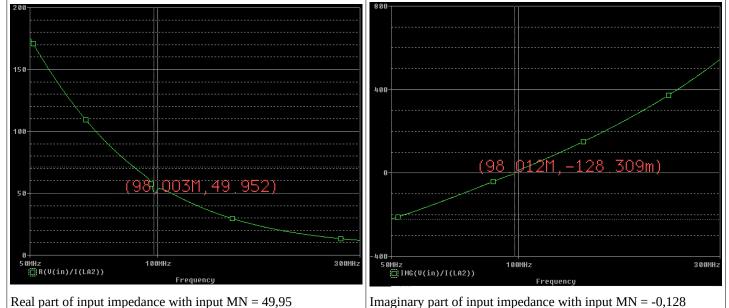
Calculate the values of the input matching network (L type) in order to have the maximum power transfer.

Source Resistance:	50 Source Reactance: 0		
Load Resistance:	90.59 Load Reactance: -263		
Desired Q: 3 Frequency: 98e6			
LOWPASS Low-Hi MATCHING NETWORK			
Rs+jXs C T Rs+jXs C Rl+jX1			
L Value: 325.6431 nH			
C Value: 2.105062 pF			
Q Value: 4.010309			

Insert the matching network in the circuit as in Fig. 4. Plot the frequency response of the amplifier in the range 50MHz – 300MHz and determine the resonance frequency, the maximum gain and the quality factor Q of the circuit.



Plot the real and imaginary parts of the input impedance of the amplifier.



Comment on the results.

It is possible to note that using the output matching network based upon Rs=2000, it was necessary to change the input L matching network.

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POINT 5, 6

In order to increase the circuit selectivity, insert the input transformer TX2 with an input inductance of 100nH and an unitary transformation ratio together with the capacitor CT2.

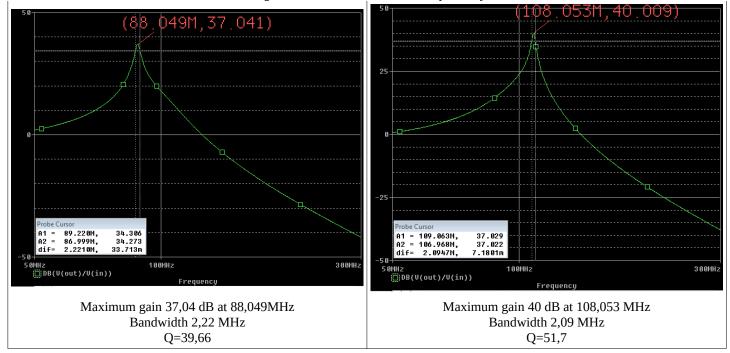
Compute the values of the capacitance CT1 and capacitance CT2 in order to have the minimum and maximum resonance frequencies at 88MHz and 108MHz.

Resonant frequency = 88 MHz CT1_max=CT2_max=33,3 pF

Resonant frequency = 108 MHz CT1_min=CT2_min=20,1pF

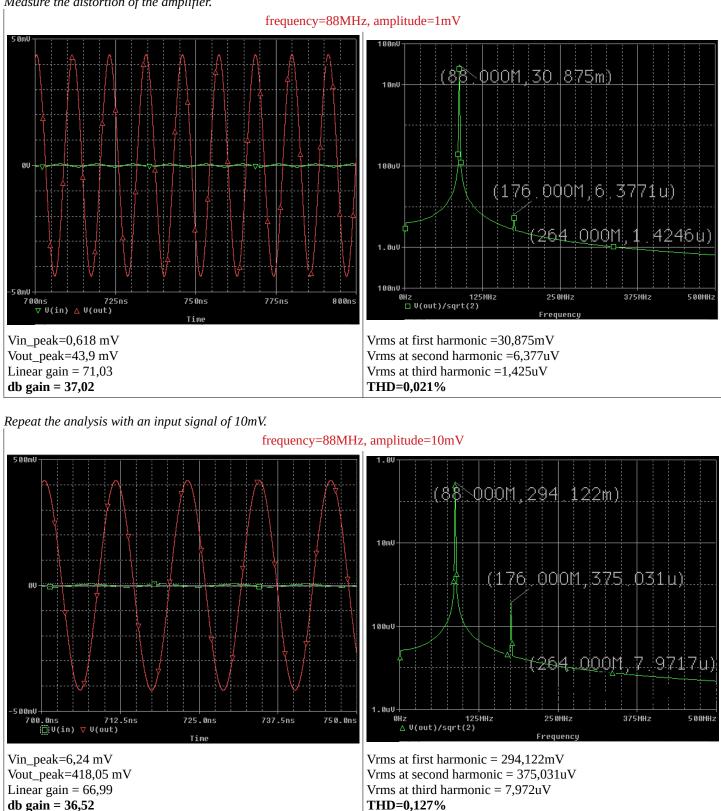
POINT 7

Simulate the circuit obtained and determine the gain at 88MHz and 108MHz, respectively.



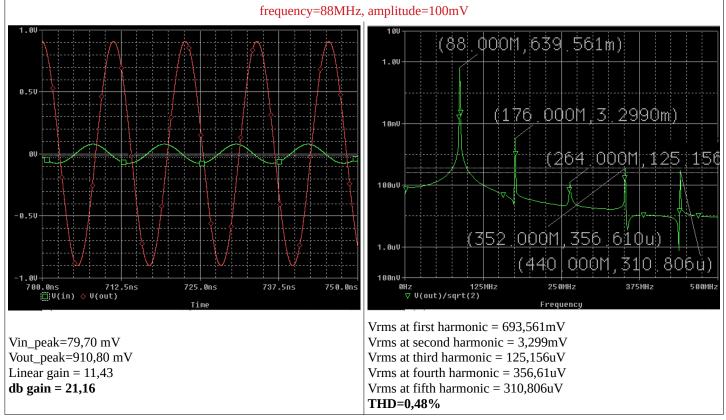
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POINT 8

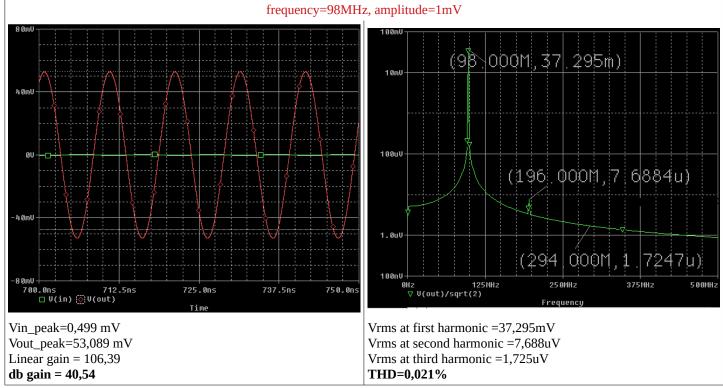


Perform a transient analysis on the circuit designed. Impose a sinusoidal input generator with amplitude of 1mV and frequency 88MHz. Moreover, set the value of the capacitances in order to get the resonance at 88MHz. Display the input and output voltages. Measure the distortion of the amplifier.

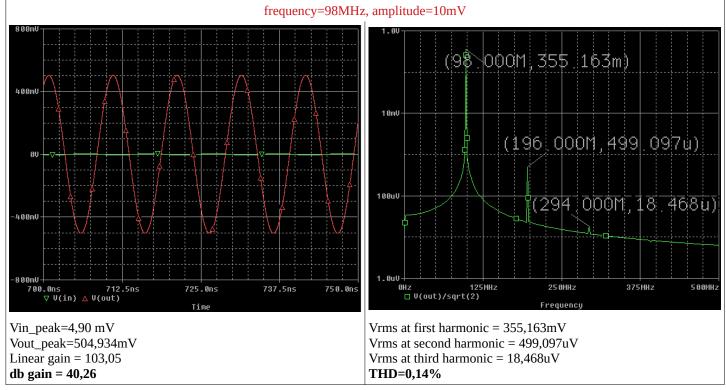
Repeat the analysis with an input signal of 100mV.



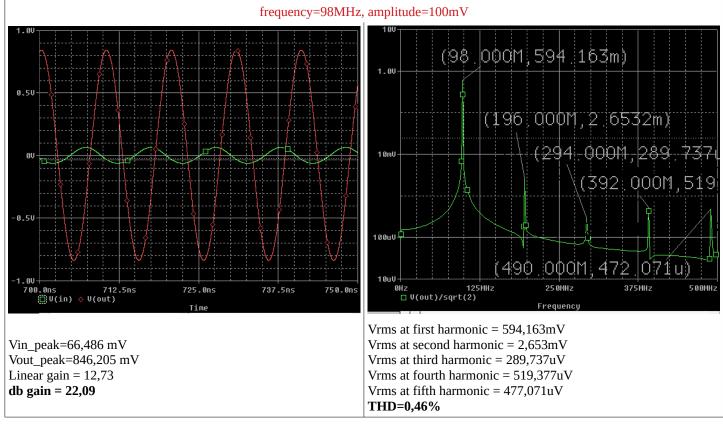
Impose a sinusoidal input generator with amplitude of 1mV and frequency 98MHz. Moreover, set the value of the capacitances in order to get the resonance at 98MHz. Display the input and output voltages. Measure the distortion of the amplifier.

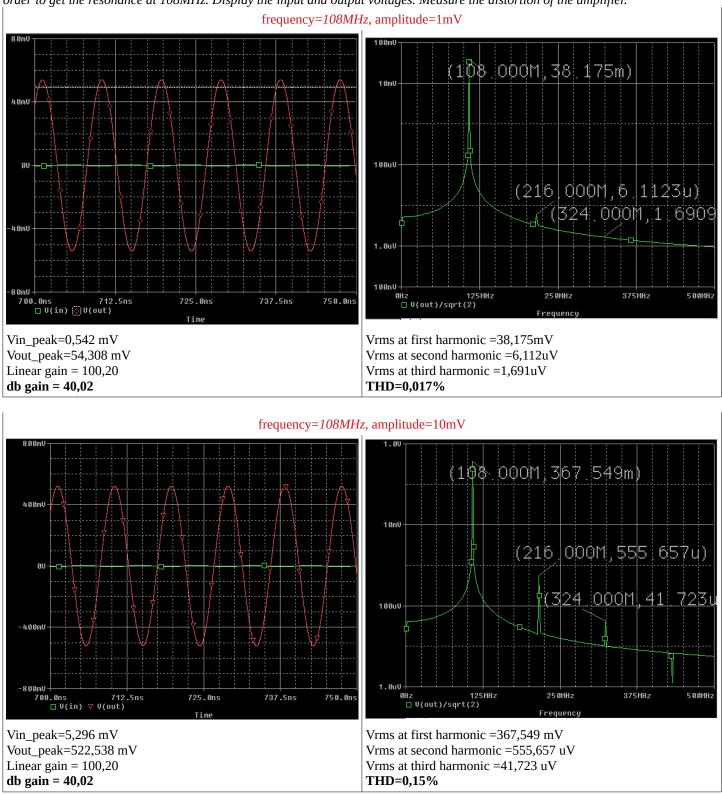


Repeat the analysis with an input signal of 10mV.

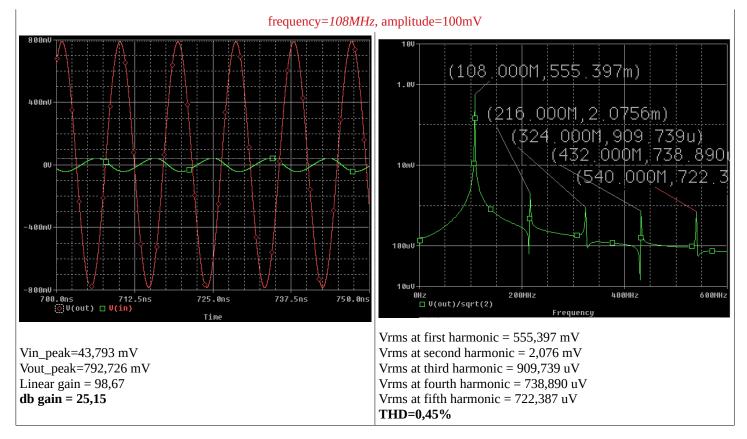


Repeat the analysis with an input signal of 100mV.





Impose a sinusoidal input generator with amplitude of 1mV and frequency 108MHz. Moreover, set the value of the capacitances in order to get the resonance at 108MHz. Display the input and output voltages. Measure the distortion of the amplifier.



POINT 9

Extract CP 1dB, IIP3 and OIP3 from data obtained in the previous step for the frequency 88MHz.

